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TITLE: METHOD AND CIRCUIT FOR ALLOCATING MEMORY ARRANGEMENT ADDRESSES
INVENTOR: GEORG BRAUN, ET AL.

SEP 13 2004 PATENT & TRADEMARK OFFICE

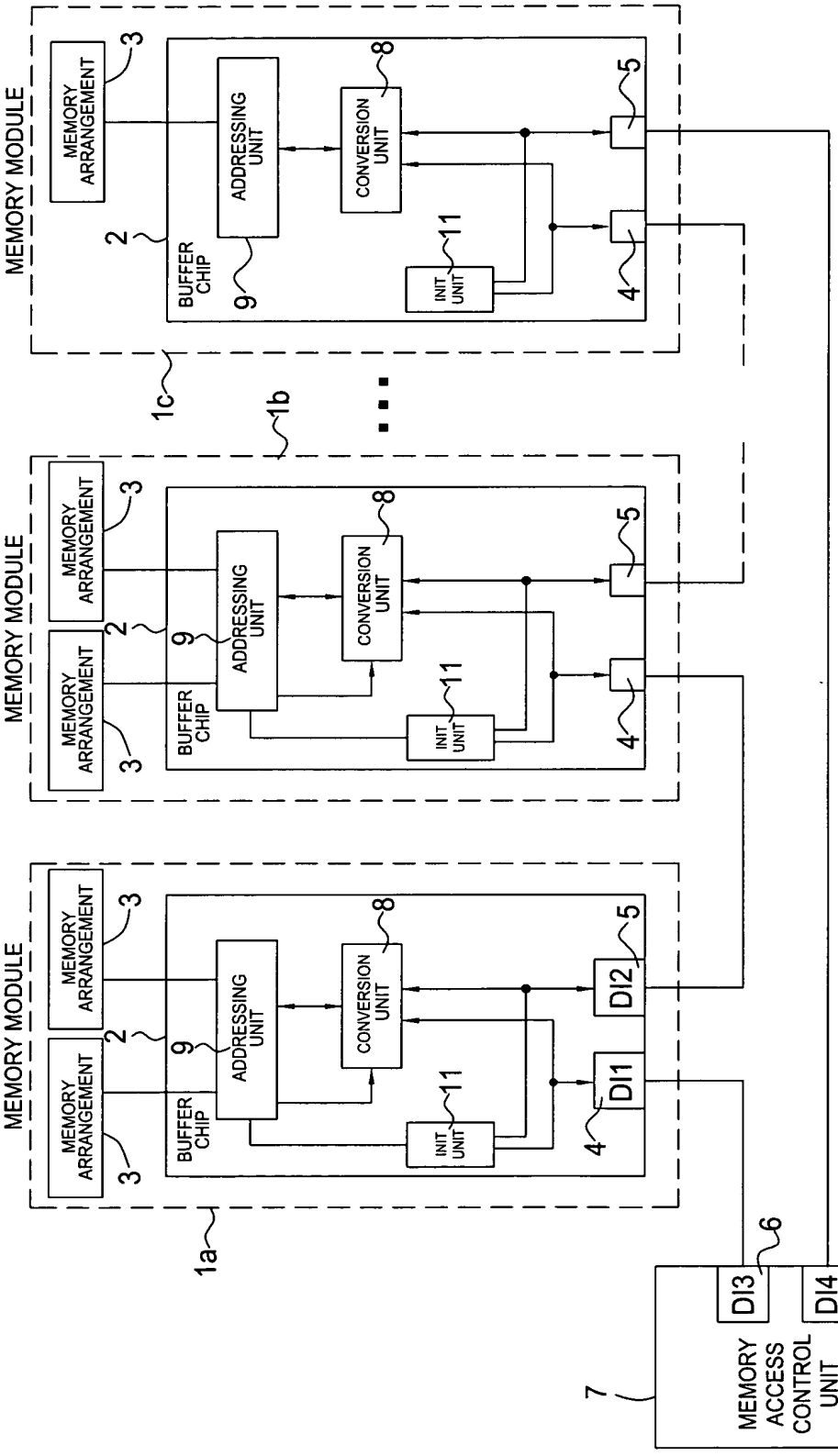

FIG. 1

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		COMMAND			
		0	1	2	3
COMMAND BITS	CA[0]	R[0]	HUB	A[0]	A[8]
	CA[1]	R[1]	RAS	A[1]	A[9]
	CA[2]	R[2]	CAS	A[2]	A[10]
	CA[3]	R[3]	WE	A[3]	A[11]
	CA[4]	R[4]	B[0]	A[4]	A[12]
	CA[5]	R[5]	B[1]	A[5]	A[13]
	CA[6]	R[6]	B[2]	A[6]	A[14]
	CA[7]	R[7]	B[3]	A[7]	A[15]

FIG. 2

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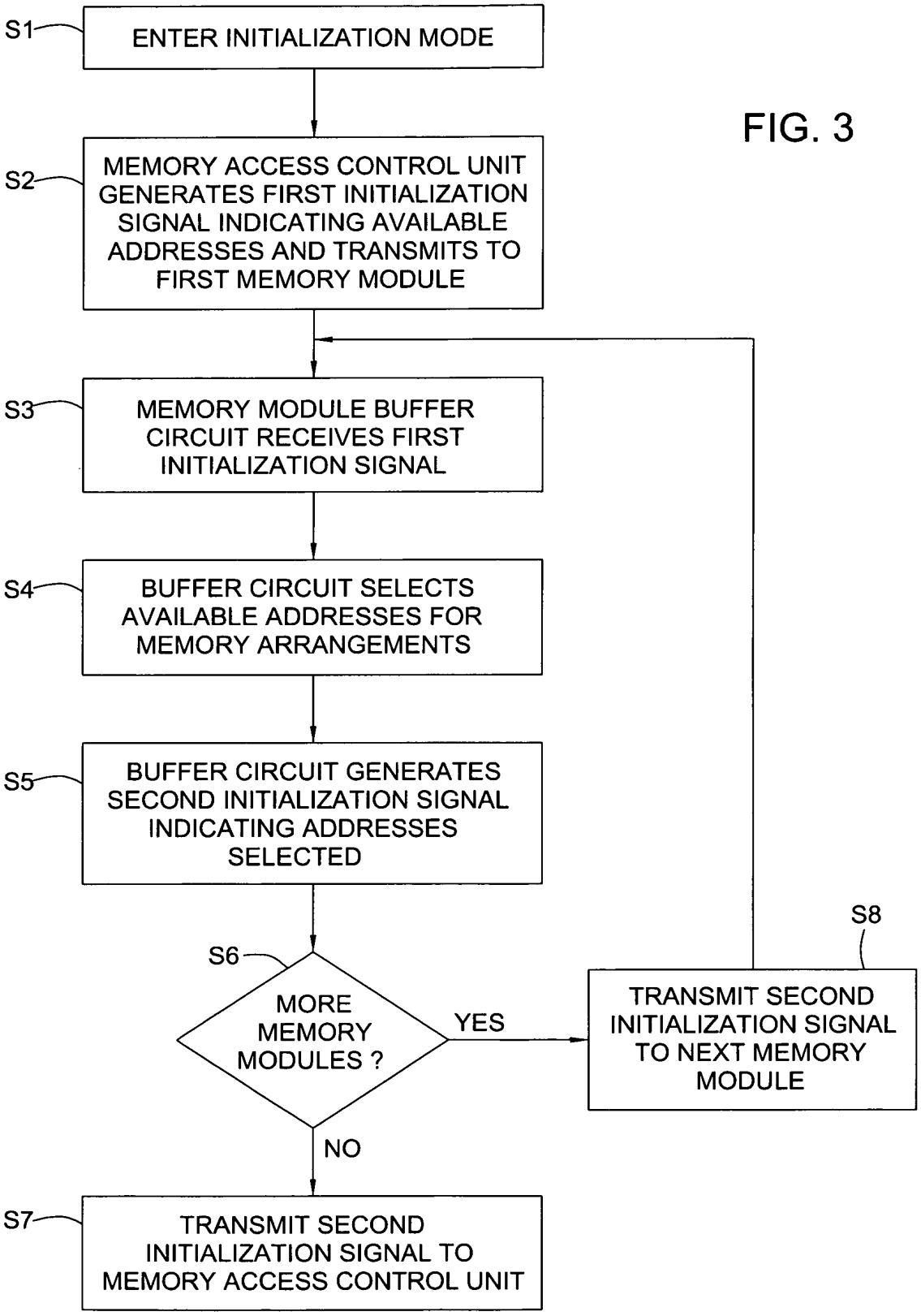

FIG. 3